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OCT 29 2003 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:
Peyman Hadizad

: Date: 10/27/2003

APPLN. NO. 10/623,392

: Group: 2812

FILED: 7/18/2003

:

FOR: METHOD OF MAKING A VERTICAL COMPOUND SEMICONDUCTOR FIELD EFFECT
TRANSISTOR DEVICE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE
UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE
ADDRESSED TO: COMMISSIONER OF PATENTS AND TRADEMARKS
P.O. Box 1450, Alexandria, VA 22313-1450, ON: OCT. 27, 2003

Date of Deposit

Lydia McNamara SEMICONDUCTOR COMPONENTS INDUSTRIES, L.L.C.
Name of Person Signing Certificate Name of Assignee
Lydia McNamara 10/27/03
SIGNATURE DATE

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

SUBMITTAL OF FORMAL DRAWINGS

Honorable Commissioner of Patents and Trademarks,
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Official Draftsperson

SIR:

Enclosed are five (5) sheets of formal drawings for the above-identified
patent application as required by 37 CFR 1.84 and 37 CFR 1.121. The copy of
the notice is returned herewith.

Respectfully submitted,

Peyman Hadizad

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